

OFSP(V) and OHSP(V) SERIES

- 14 pin and 8 pin Dip Thru-hole
- 5.0, 3.3 and 2.5 Volt
- LVPECL or LVDS Output



Electrical Specifications

Frequency Range:	-	0.75MHz to 800.000MHz
Temperature Stability:	-	See part numbering guide options below
Aging:	-	±3ppm Year Maximum
Operating Temperature Range:	-	See part numbering guide options below
Storage Temperature Range:	-	-55°C to +105°C
Supply Voltage (Vdd):	LVPECL or LVDS	See part numbering guide options below
Supply Current:	24.000MHz - 800.000MHz	25mA Maximum - 100mA Maximum
Output Voltage Logic "0" (Vol):	2.5Vdc ±5% or 3.3Vdc ±5%	Vdd -1.620Vdc Minimum (LVPECL); 1.43V Typical (LVDS)
Output Voltage Logic "1" (Voh):	2.5Vdc ±5% or 3.3Vdc ±5%	Vdd -1.025Vdc Minimum (LVPECL); 1.10V Typical (LVDS)
Duty Cycle:	at 50% of waveform	See part numbering guide options below
Load Drive Capability:	-	50Ohms
Rise/Fall Time:	at 20% to 80% waveform	1nSec Maximum
Jitter:	at 12kHz to 20MHz (RMS)	1.0pSec Maximum
Tri-state Input Voltage	-	V _{IH} of 70% of V _{CC} Minimum or No Connect to Enable Output and Complementary Output, V _{IL} of 30% of V _{CC} Maximum to Disable Output and Complementary Output (High Impedance).

Mechanical Dimensions

14 Pin Dip

ALL DIMENSIONS IN MILLIMETERS

Pad	FUNCTION
1	N/C, Tri-State or Comp Output
7	Case Ground
8	Output
14	Supply Voltage

8 Pin Dip

MARKING

Line 1: Ascend
Line 2: XX.XXX (Frequency)
Line 3: XXXXXX (Date Code)

Pad	FUNCTION
1	N/C, Tri-State or Comp Output
4	Case Ground
5	Output
8	Supply Voltage

Part Numbering Guide

OFS 3 P A 1 A N - 33.000M

Series

OFS = 14 pin dip
OHS = 8 pin dip

Supply Voltage

5 = 5.0V
3 = 3.3V
2 = 2.5V

Logic Type

P = PECL / LVPECL
V = LVDS

Freq. Toler/Stab.

A = ±100PPM
B = ±50PPM
C = ±25PPM
D = ±20PPM
E = ±10PPM

Frequency

Pin 1 Connection

T = Output Enable
N = No Connect
C = Complementary Output

Duty Cycle

A = 40% / 60%
B = 45% / 55%

Temperature Range

1 = 0°C to +70°C
2 = -20°C to +70°C
3 = -40°C to +85°C